

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application of: Robert G. Wodnicki

Group Art Unit:

Serial No.: 10/605,093

Examiner:

Filed: September 8, 2003

Title: METHOD AND APPARATUS FOR HIGH-VOLTAGE SWITCHING OF
ULTRASOUND TRANSDUCER ARRAYCommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 CFR § 1.56, Applicants request that the references cited in the accompanying Form PTO/SB/08A be considered by the examiner in the above-entitled application and made of record therein. A copy of the cited references is enclosed.

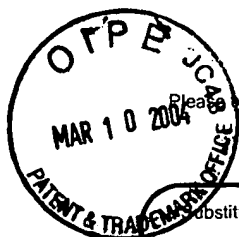
Respectfully submitted,

Dennis M. Flaherty
Registration No. 31,159March 5, 2004
DateOSTRAGER CHONG & FLAHERTY LLP
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(212) 826-6565CERTIFICATE OF MAILING

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 2 of 2

Complete if Known

Application Number	10/605,093
Filing Date	September 8, 2003
First Named Inventor	Robert G. Wodnicki
Group Art Unit	
Examiner Name	
Attorney Docket Number	GE RD 128433

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	E	PROCEEDINGS OF POWERCON 7: SEVENTH NATIONAL SOLID-STATE POWER CONVERSION CONFERENCE, SAN DIEGO, CALIFORNIA, MARCH 24-27, 1980; NATONAL SOLID-STATE POWER CONVERSION (7 TH); 1980; SAN DIEGO CALIF.. 3500 J.2.1-J.2.2.11; FRAGALE WITH USING THE POWER MOSFETS INTEGRAL REVERSE; VENTURA, CA: POWER CONCEPTS, 1980	
	F	A COPLANAR CMOS POWER SWITCH; ERNST HABEKOTTE, BERND HOFFLINGER, WOLFGANG RENKER, AND GUNTER ZIMMER, IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. SC-16 NO.2, JUNE 1981	
	G	A VERSITILE MICROPOWER IGH-VOLTAGE FLAT PANEL DISPLAY DRIVER IN A 100-V 0.7-UM CMOS INTELLIGENT INTERFACE TECHNOLOGY; JAN DOUTRELOIGNE, HERBERT DE SMET, AND ANDRE VAN CALASTER, IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 36, NO. 12, DECEMBER 2001	
	H	A MONOLITHIC 200-V CMOS ANALOG SWITCH; JAMES D. PLUMMER, AND JAMES D. MEINDL; IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. SC-11, NO. 6, DECEMBER 1976	
	I	A HIGH VOLTAGE MOS SWITCH; KRISHNA C. SARASWAT, JAMES D. MEINDL, AND JOSEF BERGER; IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. SC-10, NO. 3, JUNE 1975	

Examiner
Signature

Date
Considered

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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